

Call for Participation 3rd Workshop on Modern Circuits and Systems Technologies

March 14-15, 2014

Aristotle University Dissemination Center (KEΔEA)

Organized by the FP7 Marie Curie IAPP Project FTK – Fast Tracker for Hadron Collider Experiments and Greek National Project NANOTRIM – Continuous Transistor Sizing Toolset for Nanoscale IC Optimization

Organizing committee:

Dr. Spyridon Nikolaidis

Associate Professor

Department of Physics

Aristotle University of

Thessaloniki

+30 2310 99 8078

snikolaid@physics.auth.gr

Dr. Kostas Kordas

Assistant Professor

Department of Physics

Aristotle University of

Thessaloniki

+30 2310 99 4121

kordas@physics.auth.gr

Calliope-Louisa Sotiropoulou

Department of Physics

Aristotle University of

Thessaloniki

+30 2310 99 8774

lsoti@physics.auth.gr

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Aristotle University of Thessaloniki-Electronics Laboratory



FP7 Marie Curie Project FTK-IAPP and Greek Cooperation Project NANOTRIM organize the third scientific workshop in the area of ICT. The workshop focuses on emerging technologies in:

- High Performance Embedded Systems
- Device and Circuit Modeling
- Digital Circuits and Systems Design
- Systems and Applications

Keynote speakers:

Dr . Paola Giannetti

Director of Research, Istituto Nazionale di Fisica Nucleare (INFN), Pisa Section, Italy

on ***“High Performance Embedded Systems for High Energy Physics Experiments ”***

Dr. Sotiris Bandas

Vice President and CTO, Helic Inc., Athens, Greece

on ***“Building Chips for Better Apps: Silicon Design Technology Trends for Mobile and the Cloud”***

Professors, researchers and companies from Europe and Greece will participate presenting their research activities.

The workshop will be held on **Friday the 14th and Saturday the 15th of March** at the **Aristotle University Dissemination Center (KEΔEA)**, lecture hall 1. Opening at **9.00 a.m.**

Registration to the workshop is **free of charge** for all participants. **Registered participants will receive a certificate of attendance.**